

Form PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT <i>(Use several sheets if necessary)</i>			ATTORNEY DOCKET NO. AUS920030547US1		SERIAL NO. Not Assigned <div style="font-size: 1.5em; margin-top: 10px;">10/687,248</div>		
			APPLICANT DeWitt, Jr. et al.				
			FILING DATE <div style="font-size: 1.2em;">10/16/03</div>		GROUP ART UNIT Not Assigned <div style="font-size: 1.2em;">2112</div>		
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE		
CCL	AA	5,103,394	Apr. 7, 1992	Blasciak	395/575	Dec. 21, 1989	
	AB	6,330,662 B1	Dec. 11, 2001	Patel et al.	712/236	Feb. 23, 1999	
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	AF	5,930,508	Jul. 27, 1999	Faraboschi et al.	395/706	Jun. 9, 1997	
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DATE CONSIDERED <div style="font-size: 1.2em;">12/14/05</div>			EXAMINER <div style="font-size: 1.2em;">Christopher E. Lee</div>				
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE	
<i>CEL</i>	BB 2002/0129309	Sep. 12, 2002	Floyd et al.	714/724	Dec. 18, 2000	
	BC 2001/0032305	Oct. 18, 2001	Barry	712/34	Feb. 23, 2001	
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<i>CEL</i>	BG 2002/0019976	Feb. 14, 2002	Patel et al.	717/137	May 25, 2001	
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EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO	
<i>CEL</i>	BH JP2000029731	Dec. 8, 1999	Japan	G06F 9/38		X
<i>CEL</i>	BI JP2000347863	Dec. 15, 2000	Japan	G06F 9/38		X
OTHER PRIOR ART (including author, title, date, pertinent page, etc.)						
<i>CEL</i>	BJ	Kikuchi, "Parallelization Assist System", Joho Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.				
	BK	Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.				
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	BM	Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.				
	BN	Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 th , Phoenix, F b. 20-22, 2000, pp. 516-524.				
<i>CEL</i>	BO	IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.				
RELATED PATENT APPLICATIONS						
EXAMINER INITIAL	APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE		
<i>CEL</i>	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug	Nov. 4, 1999		
<i>CEL</i>	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System	Oct. 2, 1995		
DATE CONSIDERED <i>12/14/05</i>			EXAMINER <i>Christopher E. Lee</i>			

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